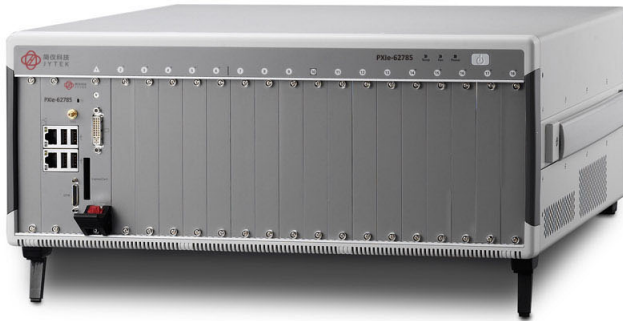


# PXIe-62785

## 18-Slot PXI Express Chassis

### User's Manual



**Manual Rev.:** 1.0

**Revision Date:** October 2, 2019

# Revision History

Revision	Release Date	Description of Change(s)
1.0	02-10-2019	Initial release

# Preface

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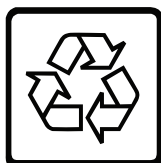
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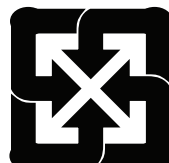
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## Battery Labels (for products with battery)



**Li-ion**



廢電池請回收

## California Proposition 65 Warning



**WARNING:** This product can expose you to chemicals including acrylamide, arsenic, benzene, cadmium, Tris(1,3-dichloro-2-propyl)phosphate (TDCPP), 1,4-Dioxane, formaldehyde, lead, DEHP, styrene, DINP, BBP, PVC, and vinyl materials, which are known to the State of California to cause cancer, and acrylamide, benzene, cadmium, lead, mercury, phthalates, toluene, DEHP, DIDP, DnHP, DBP, BBP, PVC, and vinyl materials, which are known to the State of California to cause birth defects or other reproductive harm. For more information go to [www.P65Warnings.ca.gov](http://www.P65Warnings.ca.gov).

## Trademarks

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## Conventions

Take note of the following conventions used throughout this manual to make sure that users perform certain tasks and instructions properly.



NOTE:

Additional information, aids, and tips that help users perform tasks.



CAUTION:

Information to prevent **minor** physical injury, component damage, data loss, and/or program corruption when trying to complete a task.



WARNING:

Information to prevent **serious** physical injury, component damage, data loss, and/or program corruption when trying to complete a specific task.

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# Table of Contents

<b>Revision History</b> .....	<b>ii</b>
<b>Preface</b> .....	<b>iii</b>
<b>List of Figures</b> .....	<b>vii</b>
<b>List of Tables</b> .....	<b>ix</b>
<b>1 Introduction</b> .....	<b>1</b>
1.1 Features.....	2
1.2 Specifications.....	3
1.3 Connectors, I/O and Controls .....	15
1.3.1 Front Panel.....	15
1.3.2 Rear Panel.....	16
1.3.3 Positions of Backplane Thermal Sensors.....	19
1.3.4 PCI Express.....	20
<b>2 Getting Started</b> .....	<b>29</b>
2.1 Package Contents .....	29
2.2 Power Supply Budget Considerations .....	30
2.3 Cooling Considerations.....	30
2.4 Configure Settings for Cooling.....	30
2.4.1 Fan Mode Switch.....	31
2.4.2 Fan Speed Switch .....	31
2.5 Hardware Installation .....	32
2.5.1 Installing the System Controller.....	32
2.5.2 Installing Peripheral Modules .....	37
2.6 Rack Mounting.....	40
2.6.1 Powering Up the System .....	41
2.7 PXIe-62785 Software Resources .....	42
2.7.1 JYTEK PXI Platform Services (APPS) .....	42

<b>3</b>	<b>System Management &amp; Configuration .....</b>	<b>43</b>
3.1	ChassisWatch .....	44
3.1.1	Chassis Status Monitor.....	44
3.1.2	Software Configurable PXI Trigger Bus Bridges.....	44
3.1.3	Smart Fan .....	46
3.2	Remote Management LAN Port.....	48
<b>A</b>	<b>Appendix: Maintenance.....</b>	<b>49</b>
A.1	Installation Problems.....	49
A.2	Basic Troubleshooting .....	50
A.3	Maintenance .....	51
A.3.1	Handling the Chassis.....	51
A.3.2	Cleaning the Exterior .....	51
A.3.3	Power Requirements .....	51
A.3.4	Replacing the Power Supply Module.....	52
	<b>Important Safety Instructions.....</b>	<b>55</b>
	<b>Getting Service .....</b>	<b>57</b>

## List of Figures

Figure 1-1:	Backplane PCI Express Bus Topology.....	7
Figure 1-2:	Front View .....	10
Figure 1-3:	Right View .....	11
Figure 1-4:	Left View.....	11
Figure 1-5:	Bottom View .....	12
Figure 1-6:	PXle-62785 Front Panel.....	15
Figure 1-7:	PXle-62785 Rear Panel.....	16
Figure 1-8:	PXle-62785 Backplane Thermal Sensor Positions...	19
Figure 1-9:	Star Trigger & Differential Star Bus Routing.....	21
Figure 1-10:	3U Hybrid Slot Compatible PXI-1 Peripheral Module	23
Figure 1-11:	3U PXI Express Peripheral Module .....	24
Figure 1-12:	Trigger Bus and Bus Bridges.....	25
Figure 1-13:	Trigger Bus Bridge Routing .....	26
Figure 2-1:	System Controller Slot Location (Slot 1).....	32
Figure 2-2:	Depress Module Release Latch .....	33
Figure 2-3:	Align System Controller Module .....	34
Figure 2-4:	Secure System Controller Module Latch .....	35
Figure 2-5:	Fasten System Controller Module Screws .....	36
Figure 2-6:	Insert Peripheral Module into Chassis.....	37
Figure 2-7:	Secure Peripheral Module Latch .....	38
Figure 2-8:	Fasten Peripheral Controller Module Screws .....	39
Figure 2-9:	Rack Mount Assembly.....	40
Figure 3-1:	Chassis ChassisWatch GUI Application Utility .....	44
Figure 3-2:	Trigger Bus Routing Settings.....	45
Figure 3-3:	Target Temperature Parameters .....	46
Figure 3-4:	Chassis Status Monitor.....	48

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## List of Tables

Table 1-1: Power Supply Specifications .....	3
Table 1-2: DC Power Output Specifications .....	4
Table 1-3: Backplane Slot DC Power Current .....	4
Table 1-4: PXI 10 MHz Reference Clock .....	4
Table 1-5: External 10 MHz Clock Output .....	5
Table 1-6: External Clock Input Requirements .....	5
Table 1-7: PXI Star Trigger .....	5
Table 1-8: PXI Express 100 MHz System Reference Clock .....	6
Table 1-9: PXI Differential Star Triggers .....	6
Table 1-10: Backplane Slots & Functionality .....	7
Table 1-11: Ventilation (Heat Dissipation) .....	8
Table 1-12: Cooling .....	9
Table 1-13: Mechanical Dimensions and Weight .....	10
Table 1-14: PXI Express Slot Pin Definitions .....	13
Table 1-15: Environmental Specifications .....	14
Table 1-16: Front Panel Power & Status .....	15
Table 1-17: Front Panel Indicators .....	16
Table 1-18: Rear Panel Legend .....	17
Table 1-19: Voltage Monitor / Remote Control Connector .....	18
Table 1-20: Star Trigger & Differential Star Bus Assignments .....	22
Table 1-21: PXIe-62785 Reference Clock Priority .....	27
Table 2-1: Rack Mount Assembly Legend .....	40
Table 3-1: Target Temperature Parameters Legend .....	47
Table 3-2: LAN Port Default IP Settings .....	48

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# 1 Introduction

The JYTEK PXIe-62785 chassis combines a high-performance 18-slot PXI Express backplane with a high-output power supply and a structural design that has been optimized for maximum usability in a wide range of applications. The chassis' modular design ensures a high level of maintainability and offers replaceable power supplies for high-availability applications, resulting in a very low mean time to repair (MTTR). The PXIe-62785 chassis fully complies with the PXI™-5 PXI Express Hardware Specification, offering advanced timing and synchronization features.

JYTEK PXIe-62785, using PCI Express 3.0 technology, features 24 GB/s of system bandwidth for high-throughput, peer-to-peer data transfer applications, and up to 8 GB/s bandwidth for all peripheral slots. Moreover, the chassis offers 990W power in total, and 50W of power cooling capacity per slot so that the PXIe-62785 can provide higher power budgets for high performance and power PXI modules. The PXI Express timing and synchronization functions are for high clock accuracy and external clock and trigger routing.

The PXIe-62785 is equipped with a smart system monitoring controller, reporting chassis status including fan speed, system voltages, and internal temperature, remote chassis monitoring via Ethernet port, and inhibit control.

## 1.1 Features

- ▶ PXI-5 PXI Express hardware specification Rev.1.1 compliant
- ▶ 18-slot PXI Express chassis with one system slot, one system timing slot, six hybrid peripheral slots, and ten PXI Express peripheral slots
- ▶ PXI Express System Slot routed as 2 Link Configuration x8 x16
- ▶ Up to 24 GB/s (PCI Express x8 x16 Gen3 link) system bandwidth
- ▶ Up to 8 GB/s (PCI Express x8 Gen3 link) bandwidth for all peripheral slots
- ▶ Low-jitter internal 10 MHz reference clock for PXI/PXI Express slots with  $\pm 1$  ppm stability
- ▶ Low-jitter internal 100 MHz reference clock for PXI Express slots with  $\pm 1$  ppm stability
- ▶ 0°C to 55°C extended operating temperature range
- ▶ Two BNC connectors for PXI 10MHz reference clock input and output
- ▶ Replaceable fan and power supply module for maintenance
- ▶ Variable speed fan controller optimizes cooling and acoustic emissions
- ▶ Through inhibit connector, including remote power button and voltage monitor



## 1.2 Specifications

The PXIe-62785 complies with the PXI-5 Specification Rev.1.1 and accepts all modules compliant with the PXI-5, CompactPCI, and PICMG 2.0 specifications.

### Power Supply

<b>AC Power Input</b>	
Input rating	100 to 120 VAC, 50-60Hz, 1100W max. 220 to 240 VAC, 50-60Hz, 1300W max.
Operating voltage range	100 to 240 VAC
Nominal input frequency	50/60 Hz
Operating frequency range	50-60 Hz
Over-current protection	16 A resettable breaker in AC input line
<b>Input current rating</b>	
110 VAC	10 A
220 VAC	6 A

**Table 1-1: Power Supply Specifications**

## DC Power Output

Operation Ambient Temperature		Maximum Total DC Output	
45C to 55°C, 113°F to 131°F		990 W max. (200 to 240VAC), 790 W max. (100 to 120VAC)	
0C to 45°C, 32°F to 113°F		1170 W max. (200 to 240VAC), 1000 W max. (100 to 120VAC)	
Voltage Rail	Maximum Total Current	Load Regulation	Maximum Ripple Noise
+12V	72A	±5%	120mV
+3.3V	77A	±5%	50mV
+5V	20A	±5%	50mV
-12V	4A	±5%	120mV
+5Vaux	4A	±5%	50mV

**Table 1-2: DC Power Output Specifications**

## Backplane Slot DC Power Current

Slot Type	Slot Number	+5V	+3.3V	+12V	-12V	+5Vaux
PXle System Slot	1	15A	15A	30A	-	3A
PXle Peripheral/ System Timing Slots	2,3,4,5,6,10,14, 15,16,17,18	-	9A	6A	-	1A
PXle Hybrid Peripheral Slot	7,8,9,11,12,13	6A	9A	6A	1A	1A

**Table 1-3: Backplane Slot DC Power Current**

## PXI 10 MHz System Reference Clock (PXI\_CLK10)

Item	Detail
Maximum slot-to-slot skew	280 ps
Accuracy	±1 ppm max, 0°C to 55°C, 32°F to 131°F
Maximum jitter	0.44 ps RMS phase-jitter (12k Hz–20 MHz)
Duty-factor for PXI_CLK10	45% to 55%
Unloaded signal swing	3.3V ± 5% (±0.165V)

**Table 1-4: PXI 10 MHz Reference Clock**

## External 10 MHz Clock Output

Item	Detail
Accuracy	$\pm 1$ ppm max, 0°C to 55°C, 32°F to 131°F
Maximum jitter	2.14 ps RMS phase-jitter (12k Hz–20 MHz)
Output amplitude	1 Vpp $\pm 20\%$ square-wave into 50 $\Omega$ , 2 Vpp unloaded
Output impedance	50 $\Omega \pm 5\Omega$

**Table 1-5: External 10 MHz Clock Output**

## External 10 MHz Clock Source Input Requirements

Item	Detail
Frequency input	10 MHz $\pm 25$ PPM
Input signal (10MHz REF In BNC)	100 mVPP to 5 VPP (square or sine)
Input impedance (10MHz REF In BNC)	50 $\Omega \pm 5\Omega$
Input signal (PXI_CLK10_IN on 10th slot)	5 V or 3.3 V TTL signal

**Table 1-6: External Clock Input Requirements**

## PXI Star Trigger

Item	Detail
Maximum slot-to-slot skew	250ps
Backplane characteristic impedance	50 $\Omega \pm 5\Omega$

**Table 1-7: PXI Star Trigger**

## PXle 100 MHz System Reference Clock (PXle\_CLK100)

Item	Description
Maximum slot-to-slot skew	100 ps
Accuracy	±1 ppm max, 0°C to 55°C , 32°F to 131°F
Maximum jitter	2.41 ps RMS phase-jitter (12k Hz–20 MHz)
Duty-factor for PXle_CLK100	45% to 55%
Absolute differential voltage (when terminated with a 50 Ω load to 1.30 V or Thévenin equivalent)	400 to 900 mV
PXle_SYNC100	Implemented as default behavior. PXle_SYNC_CTRL is disabled by connecting a 10kΩ pull-down resistor to ground.

**Table 1-8: PXI Express 100 MHz System Reference Clock**

## PXI Differential Star Triggers

(PXle-DSTARA, PXle-DSTARB, PXle-DSTARC)

Item	Detail
Maximum slot-to-slot skew	150ps
Maximum differential skew	25ps
Backplane differential impedance	100Ω ±10Ω
Maximum Channel Bandwidth	1.75GHz

**Table 1-9: PXI Differential Star Triggers**

## Backplane Slots & Functionality

Type of Slot	Qty	Slot Number	Bus
PXI Express System Slot	1	1	2 Link x8 x16 (Note 1)
PXI Express System Timing Slot	1	10	(Note 2)
PXI Express Hybrid Peripheral Slot	6	7,8,9,11,12,13	(Note 2)
PXI Express Peripheral Slot	10	2,3,4,5,6,14,15, 16,17,18	(Notes 2, 3)

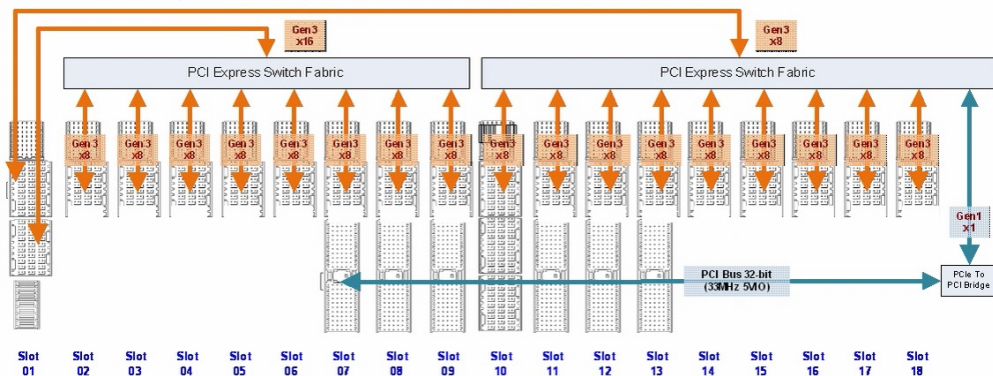
**Table 1-10: Backplane Slots & Functionality**

### Notes:

1. PCIe x8 x16 Gen3 supports up to 24 GB/s system bandwidth.
2. PCIe x8 Gen3 supports up to 8 GB/s peripheral bandwidth.
3. PCI Bus 32-bit at 33 MHz and 5 VIO.

## Backplane PCI Express Bus Topology

### PXIe-62785 Backplane PCI Express Bus Fabric- 2 Link x16 x8



**Figure 1-1: Backplane PCI Express Bus Topology**

## Ventilation (Heat Dissipation)

Usage Mode	Temperature Range	Environment
When all peripheral modules require $\leq 38$ W cooling capacity per slot, Set Fan mode as Normal.	0°C to 55°C, 32°F to 131°F (Notes 1, 3)	Indoor use only
When any of the peripheral modules require 38 to 50 W cooling capacity per slot, Set Fan mode as Boost.	0°C to 55°C, 32°F to 131°F (Notes 1, 3)	
When any of the peripheral modules requires $\leq 82$ W cooling capacity per slot, Set Fan mode as Boost.	0 °C to 45 °C, 32°F to 113°F (Notes 2, 3)	
Relative humidity range	10% to 90%, non-condensing (Note 4)	

**Table 1-11: Ventilation (Heat Dissipation)**

### Notes:

1. Install Wind Block Module on all vacant slots.
2. The modules in the adjacent 2 left and 2 right slots of the 82W modules should be less than 50W. If vacant, install Wind Block Module.
3. Validation complies with IEC 60068-2-1 and IEC 60068-2-2.
4. Validation complies with IEC-60068-2-30.

## Cooling

Item	Detail
Chassis Rear Fans (for peripheral modules)	Forced air circulation through 3x 220 CFM fans
Chassis Side Fan (for power module)	Power supply unit integrated fan
Module slot airflow direction	From bottom to top of module
Module airflow intake	Bottom and Side of front part of chassis
Module airflow exhaust	Rear of chassis
System Module Slot cooling capacity	Maximum 150 W
Peripheral Module Slot cooling capacity	Maximum 82 W
Power supply cooling	Forced air circulation through integrated fan
Power supply intake	Right side of chassis
Power supply exhaust	Rear of chassis
Minimum chassis cooling clearances	Rear: 101.60 mm (4.00 in.) Side: 44.45 mm (1.75 in.) Bottom: 17.00 mm (0.67 in.)

**Table 1-12: Cooling**

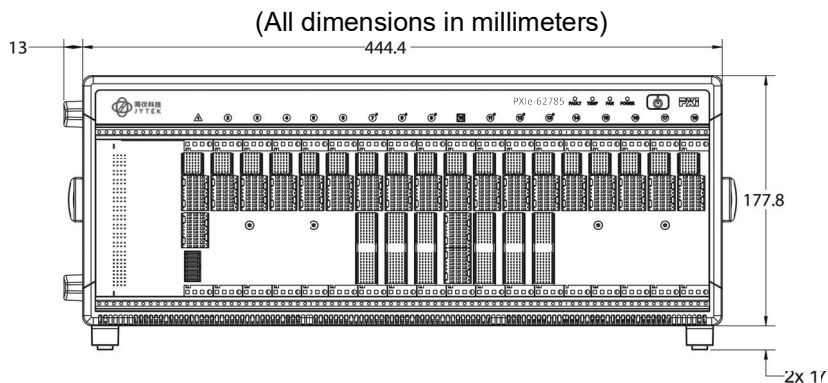


Do not block ventilation holes. Ensure sufficient cooling clearances for the front, bottom, sides, and rear of chassis.

## Mechanical Dimensions and Weight

Item	Detail
Height	177.8mm (7.000")
Width	444.4mm (17.496")
Depth	480.5mm (18.917")
Net Weight	11.9 kg (26.2 lbs)
Gross Weight (with package, power cable, and filler panels)	14.6 kg (32.2 lbs)

**Table 1-13: Mechanical Dimensions and Weight**



**Figure 1-2: Front View**



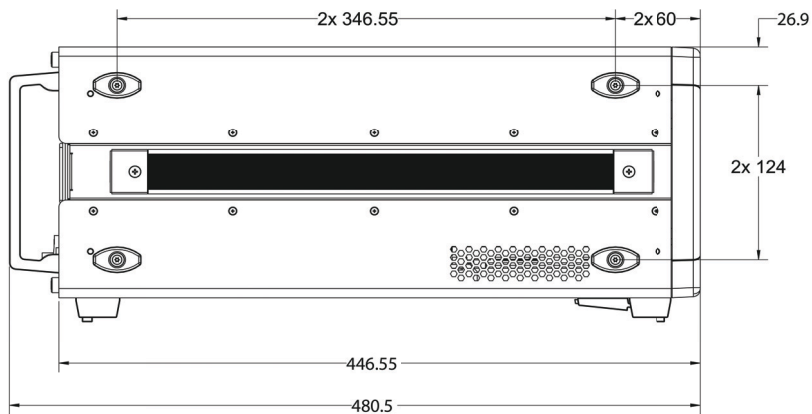


Figure 1-3: Right View

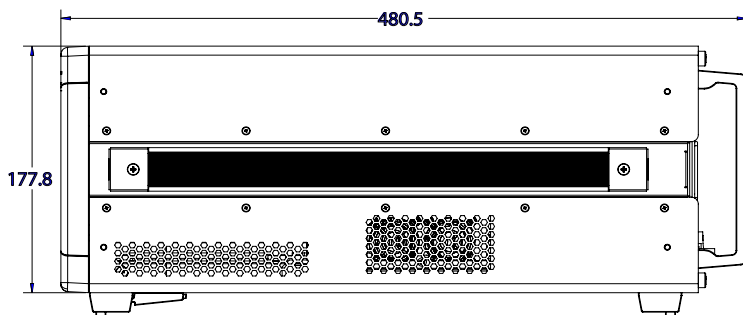


Figure 1-4: Left View

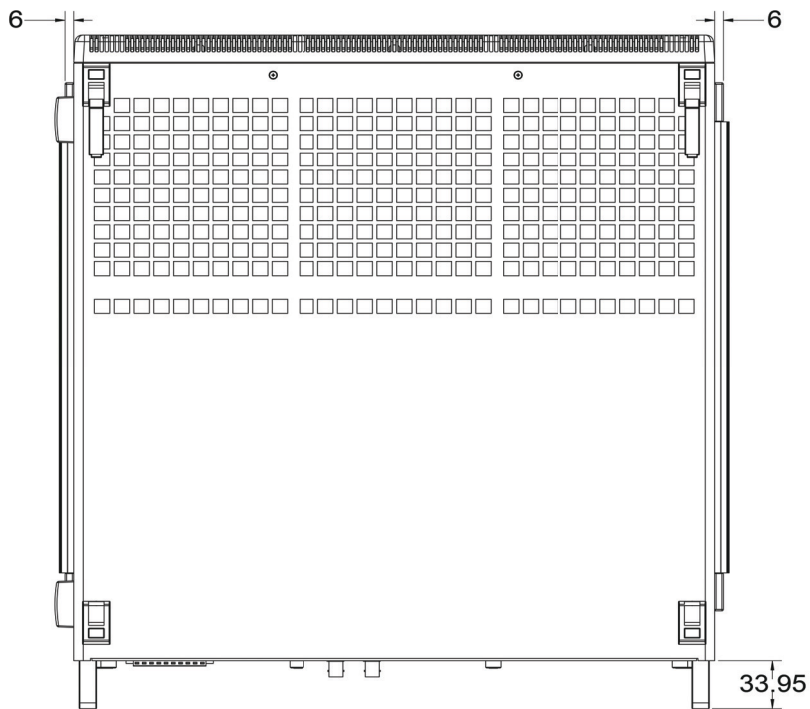


Figure 1-5: Bottom View

## Pin Definitions

Slot Type	Pin Definition
PXI Express System Slot	Comply with PXISA PXI-5 Hardware Specification: PXI Express System Slot Pin Assignments for 2 Link Operation
PXI Express System Timing Slot	Comply with PXISA PXI-5 Hardware Specification: PXI Express System Timing Slot Pinout
PXI Express Peripheral Slot	Comply with PXISA PXI-5 Hardware Specification: PXI Express Peripheral Slot Pin Assignments
PXI Express Hybrid Peripheral Slot	Comply with PXISA PXI-5 Hardware Specification: PXI Express Hybrid Peripheral Slot Pin Assignments

**Table 1-14: PXI Express Slot Pin Definitions**

## Software and Utilities

The PXIe-62785 complies with PXI-6 PXI Express Software Specification Revision 1.2. Related software resources are available in following software packages.

### **JYTEK PXI Software Service**

A software package that provides PXI-2/PXI-6/PXI-9 software services, drivers, sample programs, and utilities for JYTEK PXI Express/PXI system modules and chassis. Please refer to the JYTEK PXI Platform Service user manual for more information.

## Environmental Specifications

Item	Detail	
Storage	Ambient temperature: -20 to 70°C Relative humidity: 10 to 90%, noncondensing	
Operating	Ambient temperature: 0°C to 55°C Relative humidity: 10 to 90%, noncondensing	
Functional shock	30 G, half-sine, 11 ms pulse duration	
Random Vibration	Operating: 5 to 500 Hz, 0.3 Grms, 3 axes Nonoperating: 5 to 500 Hz, 2.46 Grms, 3 axes	
Fan Mode*	Fan Speed*	Acoustic Emissions
Normal	Auto	40.9 dBA (up to 25°C ambient)
	High	51.0 dBA
Boost	Auto	46.1 dBA (up to 25°C ambient)
	High	69.2 dBA

\*Fan Mode and Fan Speed are set by switches at the rear of the chassis.

**Table 1-15: Environmental Specifications**

## Electromagnetic Compatibility

- ▶ EN 61326-1 (IEC 61326-1): Class A emissions; Basic immunity
- ▶ EN 55011 (CISPR 11): Group 1, Class A emissions
- ▶ EN 55032 2015/AC: 2016 Class A
- ▶ EN 55024 2010+ A1: 2015
- ▶ EN 61000-3-2:2014 : Class A
- ▶ EN 61000-3-3:2013 : Class A
- ▶ EN 55024:2010+A1:2015 : Immunity
- ▶ FCC 47 CFR Part 15 Subpart A (Class A)
- ▶ ICES-001 Class A
- ▶ ICES-003 Issue 6-2016
- ▶ AS/NZS CISPR 11: Group 1, Class A emissions
- ▶ AS/NZS CISPR 32: 2015 (Ed 2.0)/C1:2016 : Class A

## 1.3 Connectors, I/O and Controls

### 1.3.1 Front Panel

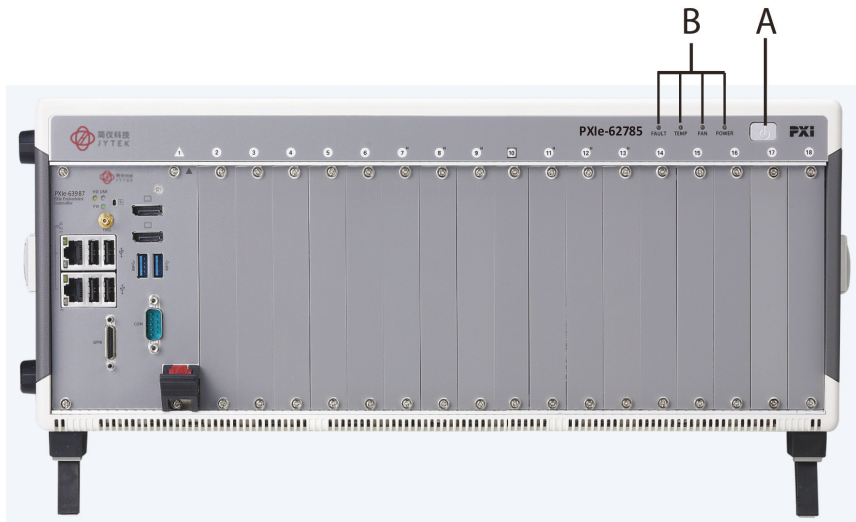


Figure 1-6: PXIe-62785 Front Panel

#### Front Panel Power & Status

Item	Feature	Description
A	Power	Powers the chassis on/off when INHIBIT on rear panel (not shown) is set to "DEF"
B	Chassis Status	Fault, Temperature Fan, Power (L to R)

Table 1-16: Front Panel Power & Status

## Front Panel Indicators

Status	Fault (Red)	Temperature (Amber)	Fan (Green)	Power (Blue)
On (Lit)	N/A	N/A	Fans operating normally	DC voltage supply is normal
Off	Normal operation	Temperature is normal	Chassis is powered down	Chassis is powered down
Blinking	Abnormal, one or more chassis readings are over threshold.	One or more temperature sensors exceeds threshold temperature (default 70°C)	One or more fans falls below threshold speed (default is 800RPM)	One or more power rails exceeds threshold settings (defaults are $\pm 5\%$ for 5V, 3.3V, +12V, and -12V)

Table 1-17: Front Panel Indicators

### 1.3.2 Rear Panel

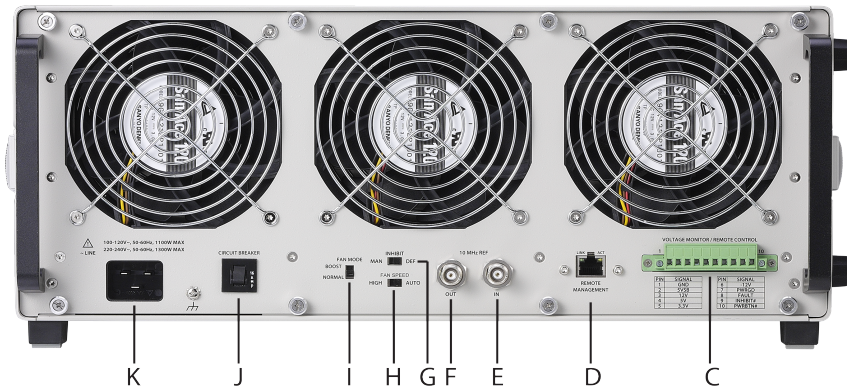


Figure 1-7: PXle-62785 Rear Panel

## Rear Panel Features

Item	Feature	Description
C	Voltage Monitor / Remote Control	See <b>Voltage Monitor / Remote Control Connector</b> on page 18 below.
D	Remote Management	The LAN port provides an HTTP web-based monitor page for displaying real time status of the PXIe-62785. The default static IP address is 192.168.1.10 and the netmask is 255.255.255.0. The IP settings can be configured via ChassisWatch. ChassisWatch is part of the JYTEK PXI Software Service. Refer to the JYTEK PXI Software Service user manual for more information.
E	10MHz Reference Clock Input	The BNC connector acts as a 10MHz reference clock input, whereby the backplane 10MHz clock is overridden in the presence of an external 10MHz clock
F	10MHz Reference Clock Output	The BNC connector acts as 10MHz reference clock output.
G	Inhibit Switch	<b>DEF:</b> Power ON/OFF (ATX mode) is controlled by the power button on the front panel and Pin10 PWRBTN# on the rear connector. <b>MAN:</b> Power ON/OFF (AT mode) is controlled by Pin9 INHIBIT#.
H	Fan Speed Switch	<b>HIGH:</b> Disables Smart Fan function. Sets fan speeds of rear 3 fans to max. RPM. <b>AUTO:</b> Enables Smart Fan function. Auto fan speed based on Fan Mode Switch setting.
I	Fan Mode Switch	<b>BOOST:</b> Sets Smart Fan to boost mode: Auto fan speed based on temperature with enhanced ventilation performance. <b>NORMAL:</b> Sets Smart Fan to normal mode: Auto fan speed based on temperature with balanced performance between ventilation and acoustic emission.
J	AC Circuit Breaker	100 to 240 VAC 16A resettable breaker in AC input line.
K	Universal Power Inlet	Accepts IEC 60320-1 C19 power outlet-equipped connection.

**Table 1-18: Rear Panel Legend**

## Voltage Monitor / Remote Control Connector

Pin	Signal	Type	Description
1	GND	GND	System Ground Pin for Pin 2 to10.
2	5VSB	Output	DC 5V stand-by monitor pin. Inline output current limit resistor 10KΩ.
3	12V	Output	DC 12V monitor pin. Inline output current limit resistor 10KΩ.
4	5V	Output	DC 5V monitor pin. Inline output current limit resistor 10KΩ.
5	3.3V	Output	DC 3.3V monitor pin. Inline output current limit resistor 10KΩ.
6	-12V	Output	Monitor pin of DC -12V. Inline output current limit resistor 10KΩ.
7	PWRGD	Output	High: Chassis power status is on. Low: Chassis power status is off or abnormal. • V <sub>OH</sub> : 2.3V min (max current 24 mA) • V <sub>OL</sub> : 0.55V max (max current 24 mA)
8	FAULT	Output	1Hz Pulse: One or more readings are over threshold settings of temperature, fan, and voltage. Low: All readings are under threshold settings. • V <sub>OH</sub> : 2.3V min (max current 24 mA) • V <sub>OL</sub> : 0.55V max (max current 24 mA)
9	INHIBIT#	Input, low active	Pin for remote power on/off chassis (AT mode). Valid when rear INHIBIT switch is set as MAN. Internal 1 kΩ pull-up to 5VSB and 10ms internal de-bounce. High or floating: Turn on all DC power output. Low: Turn off all DC power output. • V <sub>IH</sub> : 2.4V • V <sub>IL</sub> : 0.5V <b>Note:</b> Before setting the signal level for this pin to low (V <sub>IL</sub> ), first perform a shutdown on the system module (slot 1) and wait for the system module OS to shut down completely.

**Table 1-19: Voltage Monitor / Remote Control Connector**

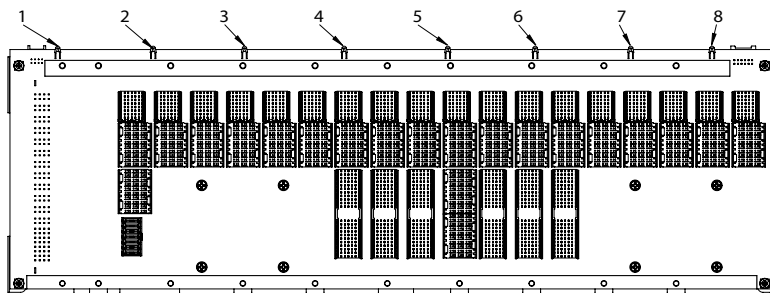


Pin	Signal	Type	Description
10	PWRBTN#	Input, low active	<p>Pin for external power button (ATX mode). Valid when rear INHIBIT switch is set as DEF. Low pulse (low duration &gt;100ms) active. Internal 1 K<math>\Omega</math> pull-up to 5VSB and 10ms internal de-bounce.</p> <ul style="list-style-type: none"> <li>• V<sub>IH</sub>: 2.4V</li> <li>• V<sub>IL</sub>: 0.5V</li> </ul>

**Table 1-19: Voltage Monitor / Remote Control Connector**

### 1.3.3 Positions of Backplane Thermal Sensors

Positions of thermal sensors.



**Figure 1-8: PXIe-62785 Backplane Thermal Sensor Positions**



NOTE:

For details about chassis temperature detection, please see “Chassis Status Monitor” on page 44.

## 1.3.4 PCI Express

### PCI Express Link Capability

The PXIe-62785 backplane provides a PCIe Gen3 switch fabric, allowing PCI express bus data streams between the system slot and all peripheral slots. The system slot supports up to PCIe 2-link x8 and x16 Gen3 lanes, with all peripheral slots supporting up to PCIe x8 Gen3 links individually.



NOTE:

Connector pin assignments of the PXI Express System Controller Slot, PXI Express System Timing Slot, PXI Express Hybrid Peripheral Slots, and PXI Express Peripheral Slots comply with the default pin assignments as defined in PXI-5 PXI Express hardware specification Rev.1.1.

---

### PXI Express System Controller Slot

The chassis System Controller slot is Slot 1 as defined by the PXI specification. The chassis can accommodate a PXI Express system controller that occupies a width up to 4 slots. As defined in the PXI specification, three controller expansion slots allow the controller to expand to the left to prevent the controller from using up peripheral slots.

## PXI Express System Timing Slot

The System Timing slot is Slot 10, providing one dedicated single-ended star trigger and 3 pairs of differential star trigger lines to each peripheral slot. Routing for single ended star trigger signals (PXI\_STAR) and differential star trigger signals (PXIe\_DSTAR) are as follows.

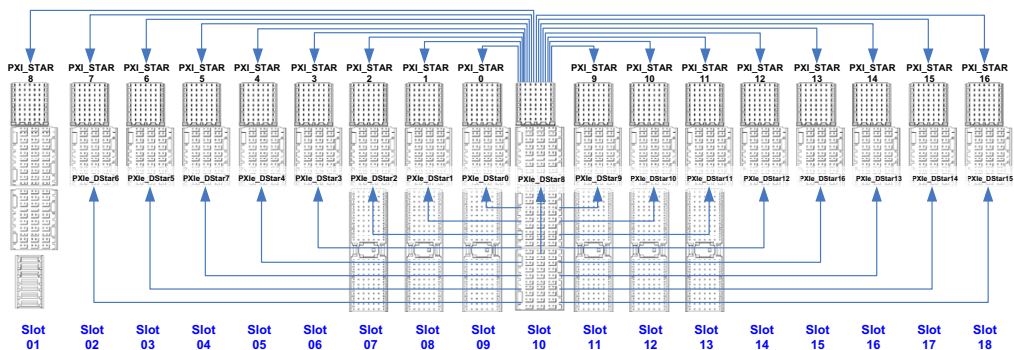


Figure 1-9: Star Trigger & Differential Star Bus Routing

Slot	PXI_Star	PXle_DStar
1	8	N/A
2	7	6
3	6	5
4	5	7
5	4	4
6	3	3
7	2	2
8	1	1
9	0	0
10	N/A	8
11	9	9
12	10	10
13	11	11
14	12	12
15	13	16
16	14	13
17	15	14
18	16	15

**Table 1-20: Star Trigger & Differential Star Bus Assignments**

Star trigger functionality provides a precise trigger signal to the peripheral modules by installation of a specific star trigger controller module in the ST slot. The system timing slot can also be used as a general PXI Express (x8 lanes) peripheral slot if star trigger functionality is not required.

## PXI Express Hybrid Peripheral Slots

6 PXI Express hybrid peripheral slots are provided in the PXIe-62785, all connected with PCIe x8 Gen3 lanes. Each can accommodate a 3U PXI Express (Figure 1-13) CompactPCI Express / hybrid slot compatible PXI-1 (Figure 1-11) Compact-PCI peripheral module.

## PXI Express Peripheral Slots

10 PXI Express peripheral slots are provided in the PXIe-62785, all slots connected with PCIe x8 Gen3 lanes. Each can only accommodate a 3U PXI Express (Figure 1-13) Compact-PCI Express peripheral module.

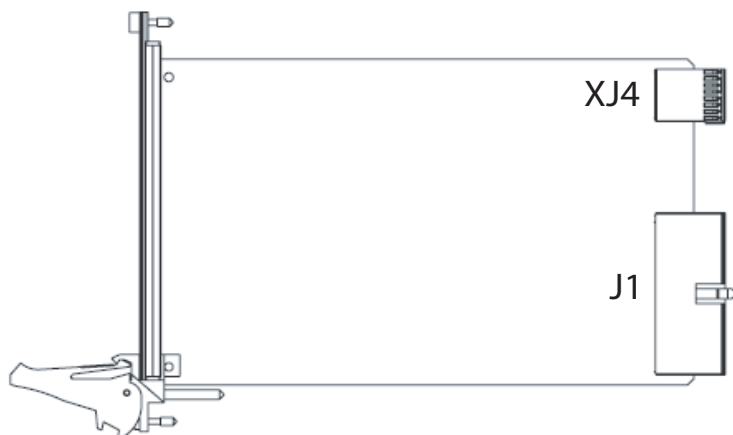
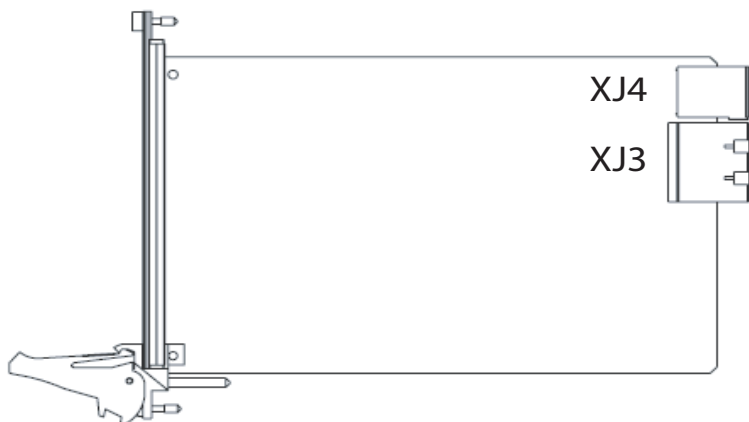


Figure 1-10: 3U Hybrid Slot Compatible PXI-1 Peripheral Module



**Figure 1-11: 3U PXI Express Peripheral Module**

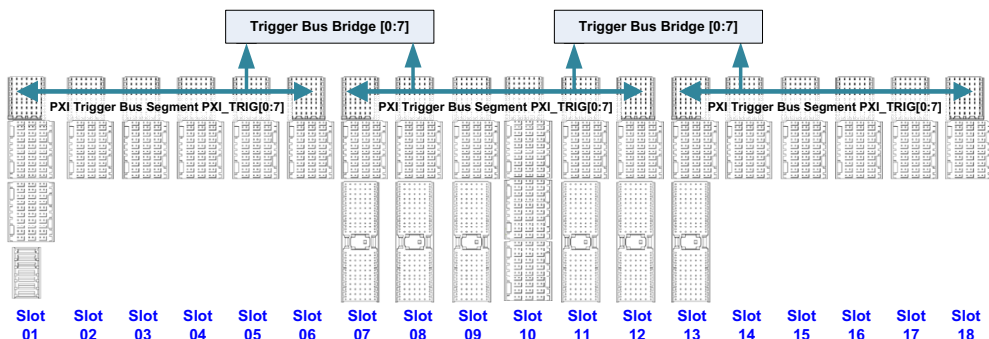
## Local Bus

The local bus on a PXI backplane is a daisy-chained bus that connects each peripheral slot with adjacent peripheral slots to the left and right. The quantity of local bus lines is decreased from thirteen to one (PXI\_LBL6 and PXI\_LBR6) on a PXI Express backplane. The remaining local bus lines can transmit analog or digital signals between modules.

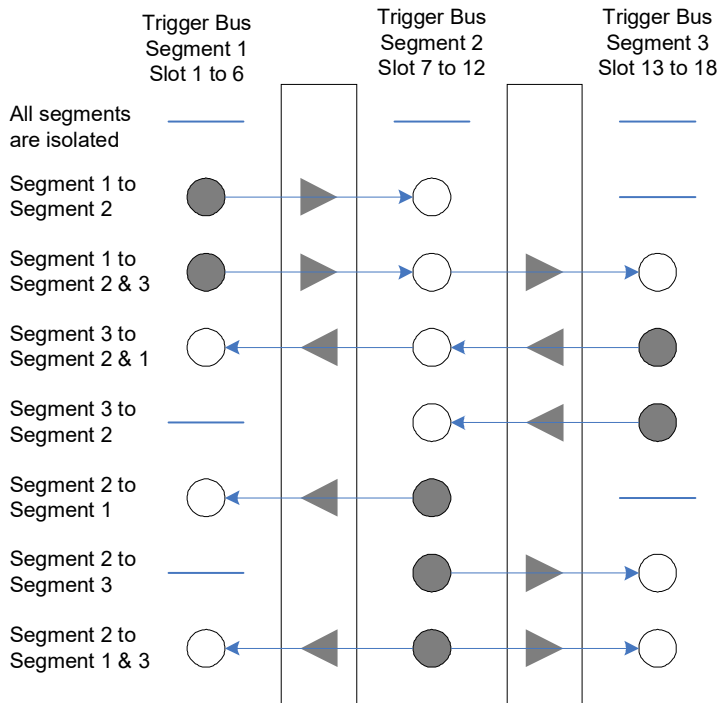
## Trigger Bus

Three trigger bus segments on the PXIe-62785 consist of a first segment from 1st to 6th slots, a second from 7th to 12th slots, and a third from 13th to 18th slots, with each trigger bus segment containing 8 trigger lines connecting all slots on the same segment, providing inter-module synchronization. PXI and PXI Express modules can exchange trigger or clock signals through the trigger bus, allowing precisely timed response to asynchronous external events.

Trigger bus bridges can be configured as connected or disconnected (default) for the trigger lines of adjacent segments. As shown, eight combinations of trigger bus segment connections are possible between the three bus segments, with any applicable to each of the eight trigger lines.



**Figure 1-12: Trigger Bus and Bus Bridges**



NOTE:

A solid circle shown represents the source of the trigger.  
A transparent circle indicates the trigger destination.

**Figure 1-13: Trigger Bus Bridge Routing**

The PXIe-62785 trigger bus bridge routing can be configured via the ChassisWatch utility, or PXI-9 C++ API function in the JYTEK PXI Platform Service. For details please see the JYTEK PXI Platform Service user manual.



## Reference Clock

The PXIe-62785 backplane supplies a single-ended 10MHz reference clock (PXI\_CLK10) and differential 100MHz clock (PXIe\_CLK100) to each peripheral slot for inter-module synchronization. The independent buffers drive the clock signal to each peripheral slot.

These common reference clock signals can synchronize multiple modules in a PXI Express chassis. PXI modules with phase-lock loop circuits can lock reference clocks to generate an in-phase timebase.

The PXI\_CLK10 and PXIe\_CLK100 clocks are in-phase according to the PXI-5 specification. Since the external 10MHz clock input can override the onboard 10MHz clock source, a phase-lock loop (PLL) circuit on the backplane synchronizes the PXIe\_CLK100 and external 10MHz clock.

The PXIe-62785 PXI chassis automatically selects the 10 MHz reference clock source from among:

- ▷ Built-in accurate 10 MHz clock source
- ▷ External 10 MHz clock through a BNC connector
- ▷ PXI\_CLK10\_IN pin on the system timing slot (slot 10)

Priorities for 10MHz reference clock are as follows.

System Timing Slot (10th slot)	BNC Connector on Rear Panel	10mhz Clock Driven to Peripheral Slots
No clock present	No clock present	10MHz clock is generated by backplane
No clock present	10MHz clock present	Clock from BNC connector is driven to all peripheral slots
10MHz clock present	No clock present	Clock from system timing slot is driven to all peripheral slots
10MHz clock present	10MHz clock present	Clock from system timing slot is driven to all peripheral slots

**Table 1-21: PXIe-62785 Reference Clock Priority**

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## 2 Getting Started

This chapter describes procedures for installing the PXIe-62785 and making preparations for its operation. Please contact JYTEK or authorized dealer if there are any problems during the installation.



NOTE:

Diagrams and illustrated equipment are for reference only. Actual system configuration and specifications may vary.

---

### 2.1 Package Contents

Before unpacking, check the shipping carton for any damage. If the shipping carton and/or contents are damaged, inform your dealer immediately. Retain the shipping carton and packing materials for inspection. Obtain authorization from your dealer before returning any product to JYTEK.

Please ensure that the following items are included in the package.

- ▶ PXIe-62785 Chassis
- ▶ Power cords
- ▶ Filler panel kit for unused/reserved slots including one 3-slot panel and eighteen 1-slot panels
- ▶ Optionally mount the chassis onto a server or similar rack using the Rack-Mount kit(G) (part number 3.91-95126-0010 PXIe-62780).

If any of these items are missing or damaged, contact the dealer from whom you purchased the product. Save the shipping materials and carton in case you want to ship or store the product in the future.



Do not install or apply power to equipment that is damaged or missing components. Retain the shipping carton and packing materials for inspection. Please contact your JYTEK dealer/vendor immediately for assistance and obtain authorization before returning any product.

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## 2.2 Power Supply Budget Considerations

Please review the power consumption of all modules used in the same PXIe-62785. The total maximum power consumed by the system module and peripheral modules shall not exceed the DC power supply of the PXIe-62785 as defined in **DC Power Output on page 4** and **Backplane Slot DC Power Current on page 4**.

## 2.3 Cooling Considerations

The PXIe-62785 features an innovative design for heat dissipation, with cooling fans in the rear section of the chassis, drawing cool air through apertures on the bottom for exhaust through the back. This design provides uniform airflow for each PXI slot and exceptional cooling capability. When the chassis is installed in a rack, the cooling design minimizes drawing of hot air from the rear area, where other devices exhaust, while maintaining a steady temperature inside the chassis. For optimal cooling efficiency, retain support feet.

When rack mounting the PXIe-62785, at least 1U (44.5 mm/1.75 in.) clearance below the intake apertures is required. Also keep other objects or equipment at a minimum of 44.45 mm (1.75 in.) away from the outlet apertures in the rear of the chassis.



NOTE:

To maintain expected air flow, please always install filler panels in unused slots. The filler panels can be found in the chassis package.

---

## 2.4 Configure Settings for Cooling

Heat ventilation is related to the installation location, environmental temperature, and total heat generated by the system module and peripherals. The PXIe-62785 features a smart fan function to provide sufficient ventilation and minimize noise emission. There are two switches located at rear of the chassis to configure the smart fan operation.

### 2.4.1 Fan Mode Switch

**Normal** - Aims to provide sufficient ventilation to peripheral modules' power to consume less than 38W per slot with controlled noise emission.

**Boost** - For peripheral modules exceeding 38W power consumption needing more ventilation.



NOTE:

Please set fan mode switch to Boost Mode if ambient temperature will be greater than 45°C/114°F.

### 2.4.2 Fan Speed Switch

**Auto** - Enables smart fan function.

**High** - Full fan rotation utilization, two profiles based on Fan Mode Switch (Normal or Boost).

The following table explains specific conditions and recommended settings.

Heat Dissipation Requirements	Operating Ambient Temperature	Recommended Settings
When all peripheral modules require ≤38 W cooling capacity per slot	0°C to 55°C, 32°F to 131°F	Sets Fan Mode as Normal, and set Fan Speed Switch to Auto.
When one of all peripheral modules require 38 to 50 W cooling capacity per slot	0°C to 55°C, 32°F to 131°F	Sets Fan Mode to Boost, and set Fan Speed Switch to Auto.
When one of all peripheral modules requires 50 to 82 W cooling capacity per slot	0°C to 45 °C, 32°F to 113°F	Sets Fan Mode to Boost, and set Fan Speed Switch to High.
Maximize ventilation for peripheral modules	Up to 45°C, 113°F	Sets Fan Speed to High, and sets Fan Mode Switch based on requirements.

## 2.5 Hardware Installation

### 2.5.1 Installing the System Controller

The PXIe-62785 incorporates a system controller slot supporting a PXI Express system controller of up to 4 slot width.

1. Ensure the CPU, memory module(s), and storage device(s) are properly installed on the system controller
2. Locate the system controller slot (Slot 1, colored red).

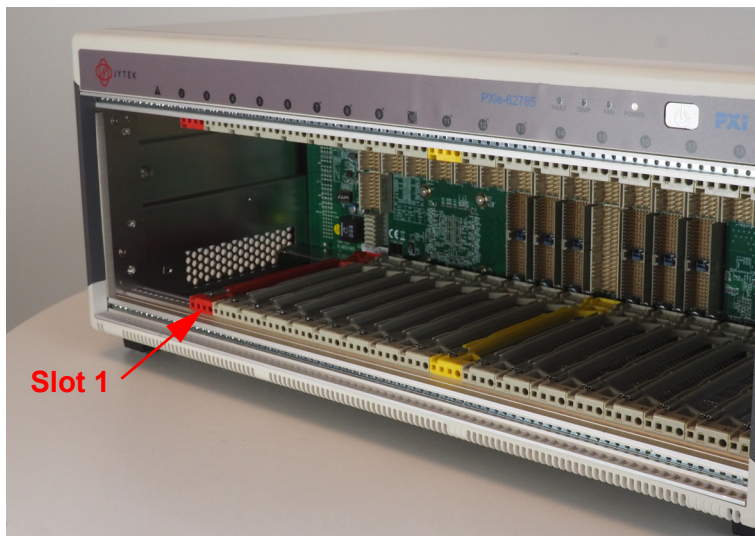
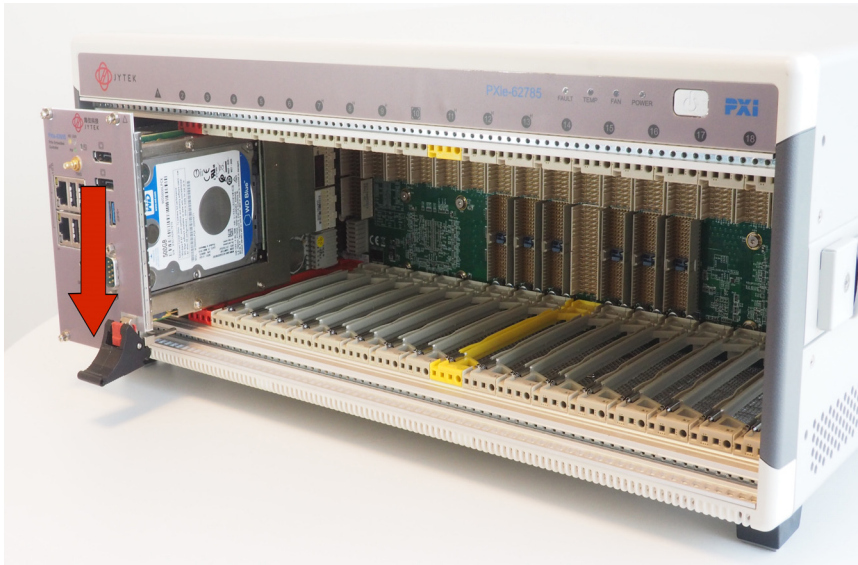


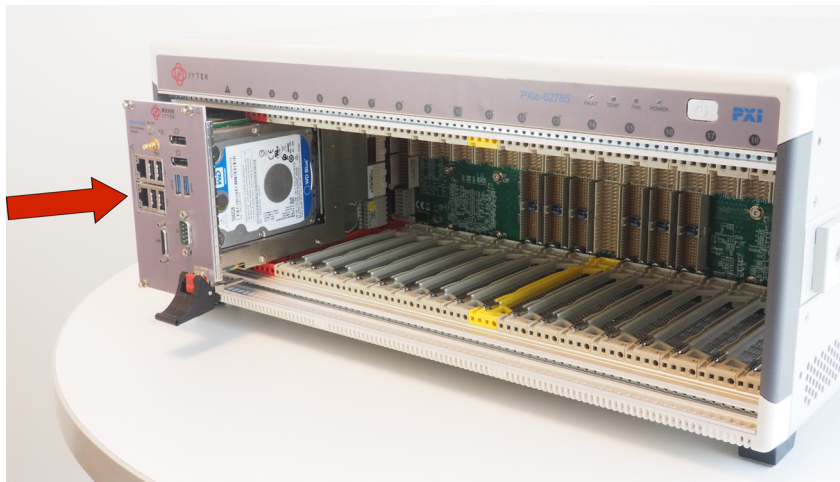
Figure 2-1: System Controller Slot Location (Slot 1)

3. On the system controller module depress the module release latch.



**Figure 2-2: Depress Module Release Latch**

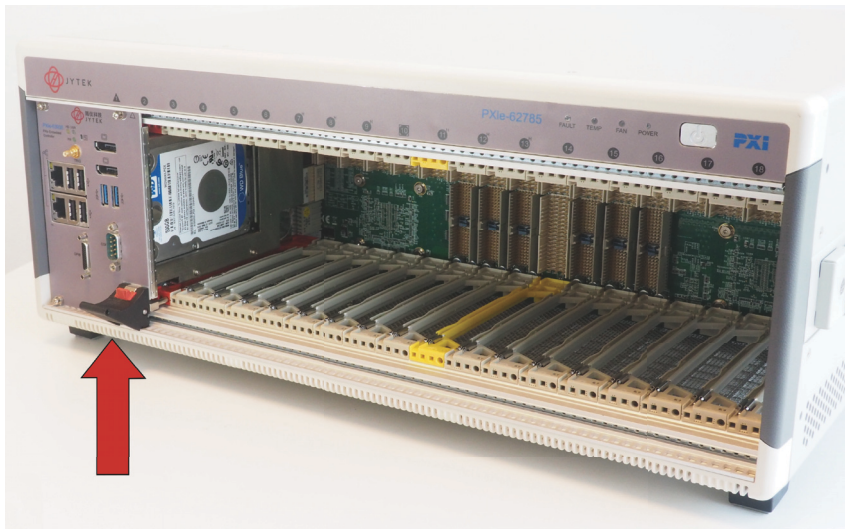
4. Align the system controller module's top and bottom edges with the card guides, and carefully slide the module into the chassis.



**Figure 2-3: Align System Controller Module**



5. Lift the system controller module latch until the module is securely seated in the chassis backplane



**Figure 2-4: Secure System Controller Module Latch**

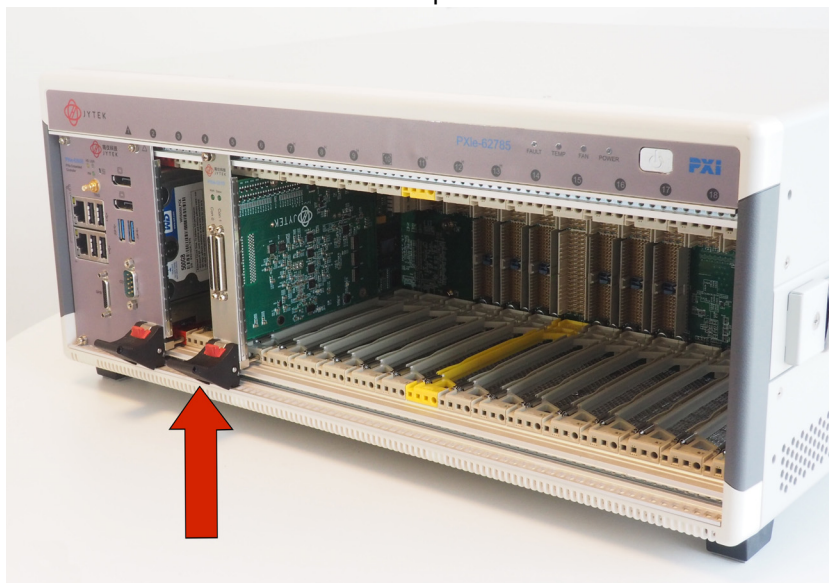
6. Fasten the screws on the module front panel, and connect all devices to the system controller.



**Figure 2-5: Fasten System Controller Module Screws**

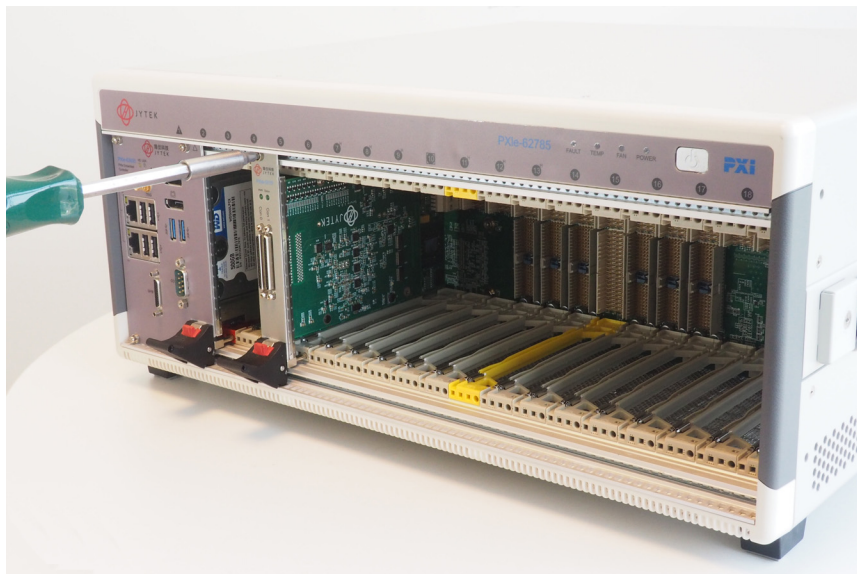


4. Lift the latch until the peripheral module is securely seated in the chassis backplane.



**Figure 2-7: Secure Peripheral Module Latch**

5. Fasten the screws on the module's front panel.



**Figure 2-8: Fasten Peripheral Controller Module Screws**

6. Repeat steps 1 to 5 to install additional PXI or PXIe peripheral modules.



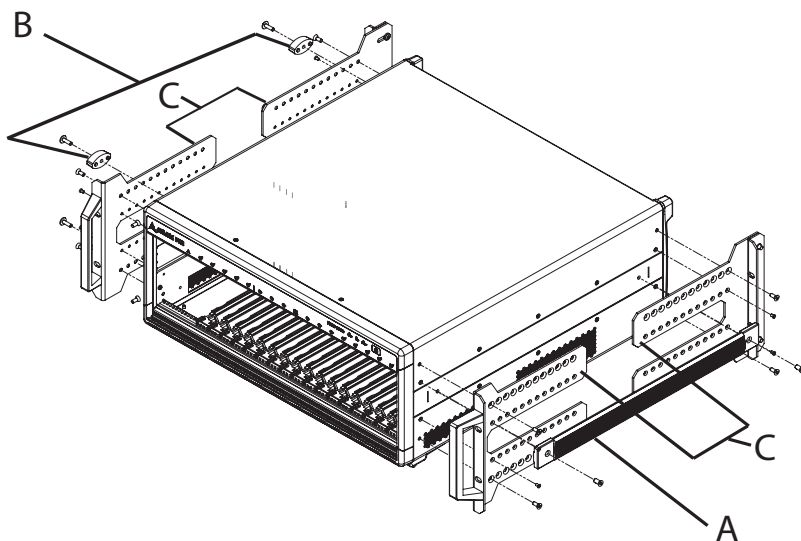
NOTE:

To improve efficiency of heat dissipation, after installing all PXI modules, please install filler plates for any unused slots.

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## 2.6 Rack Mounting

JYTEK provides hardware for optional installation of the PXIe-62785 to a rack. When mounted in a rack, the Rack-Mount kit(G) (part number 3.91-95126-0010 PXIe-62780) allows for adjustable placement of the PXIe-62785 further back in the rack, thus allowing ample room at the front of the unit for external mechanical parts (such as cables and mass interconnect modules).



**Figure 2-9: Rack Mount Assembly**

Feature	Description
A	Carrying Handle
B	4 Rubber Feet (only 2 shown)
C	Mounting Brackets

**Table 2-1: Rack Mount Assembly Legend**

To rack mount the PXIe chassis, follow the steps below:

1. Remove carrying handle from the chassis right side.
2. Remove the four rubber feet from the chassis left side. Store the feet for later use, as they will not be needed for rack mount use.
3. Install the mounting brackets on both sides of the chassis using the provided M4 screws. Position of the mounting brackets can be adjusted along the screw holes to position the chassis forward or backward as needed.
4. Install the chassis and kit into the rack using eight screws (not included).

### 2.6.1 Powering Up the System

The PXIe-62785 is equipped with a 100 VAC to 240 VAC universal power supply unit requiring no input voltage selection.



Do not to perform "hot swapping", replacement, disconnecting or connecting of any components (including cards and cabling) on chassis while the system is powered up. By not observing this Warning, system damage and/or data loss, and physical injury (due to possible shock hazard) may result.

---

To power up the system:

1. Connect one end of the supplied power cord to the power inlet located at the rear side of the chassis.
2. Plug the other end of the AC power cord to a properly grounded wall socket or power strip.
3. Press the power button. The Power LED (blue) lights up immediately.

## 2.7 PXIe-62785 Software Resources

The following software resources are available for various application requirements.

### 2.7.1 JYTEK PXI Platform Services (APPS)

JYTEK PXI Platform Services is a software kit consisting of a utility and a group of software services and drivers.

- ▶ **JYTEK ChassisWatch**

A GUI application program providing chassis status monitoring and configuration functions that are exclusive for JYTEK PXI Express chassis (PXIe-62785, PXIe-62780, PXIe-62590, PXIe-62301).

- ▶ **PXI Express Chassis Software Services**

API services and drivers for the PXIe-62785 defined in the PXISA PXI-6 Software Specification.

- ▶ **PXI Resource Manager**

A Windows background service following the PXI-2 and PXI-6 Software Specification to scan PXI/PXIe controllers, chassis, and peripheral modules and generate PXI system description files accordingly.

- ▶ **PXI Trigger Management Software Services**

API services and Trigger Manager defined in PXISA PXI-9 PXI and PXI Express Trigger Management Specification.

Please refer to the JYTEK PXI Platform Services user manual for more information.



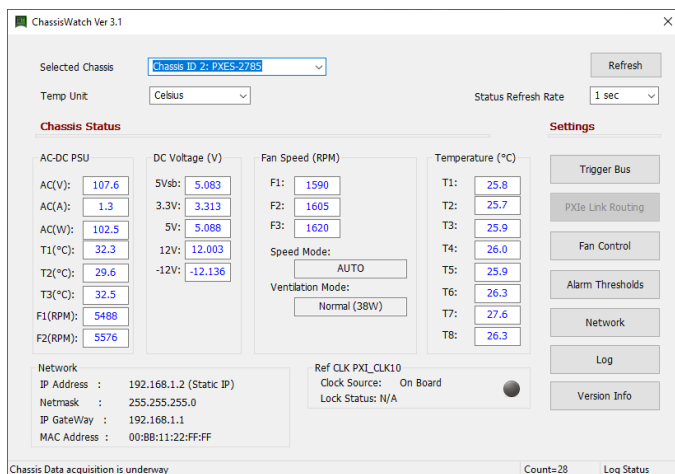
## 3 System Management & Configuration

The PXIe-62785 chassis provides advanced system monitoring and control. Chassis conditions, including internal temperature, fan speed, and DC voltage can all be monitored via JYTEK ChassisWatch — a Windows based utility for JYTEK PXIe chassis monitoring and configuration.

Communication with the chassis monitoring control unit is available using an embedded controller, such as the JYTEK PXIe-63987, via JYTEK ChassisWatch to access the SMBus located on the system slot (1st slot), or, alternatively, using a remote PC to communicate via the LAN port (RJ-45 connector on the rear panel).

## 3.1 ChassisWatch

The PXIe-62785 chassis provides advanced system monitoring and control via the JYTEK ChassisWatch GUI application. Related functions such as status monitoring, configurable PXI trigger bus bridges, and Smart Fan are introduced in the following sections. For more information about ChassisWatch, please refer to the JYTEK PXI Platform Services user manual.



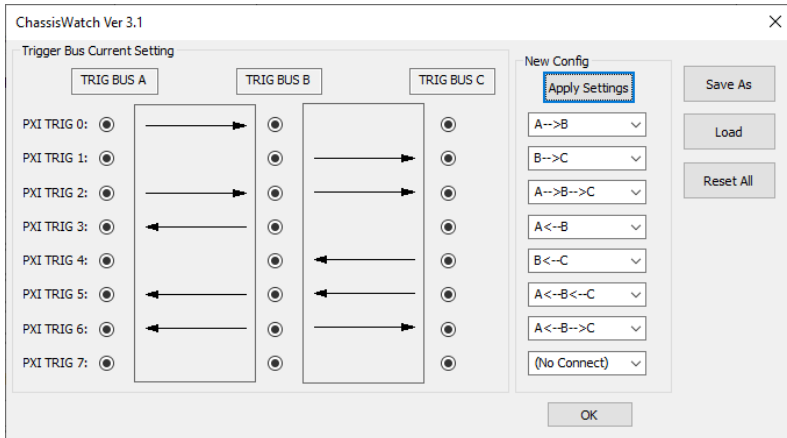
**Figure 3-1: Chassis ChassisWatch GUI Application Utility**

### 3.1.1 Chassis Status Monitor

Chassis conditions, including internal temperature, fan speed, and DC voltage can all be monitored by ChassisWatch running on an embedded PXI system controller or remote PC.

### 3.1.2 Software Configurable PXI Trigger Bus Bridges

The PXIe-62785 provides software configurable trigger bus bridges, whereby the user can set the status of each trigger bus line as shown in Figure 3-2. Between trigger bus segments, TRIG BUS A (slot 1 to 6), B (slot 7 to 12), and C (slot 13 to 18), each PXI trigger bus line can be individually configured as different routing topologies. Trigger bus routing settings can be easily saved as templates and loaded quickly.



**Figure 3-2: Trigger Bus Routing Settings**

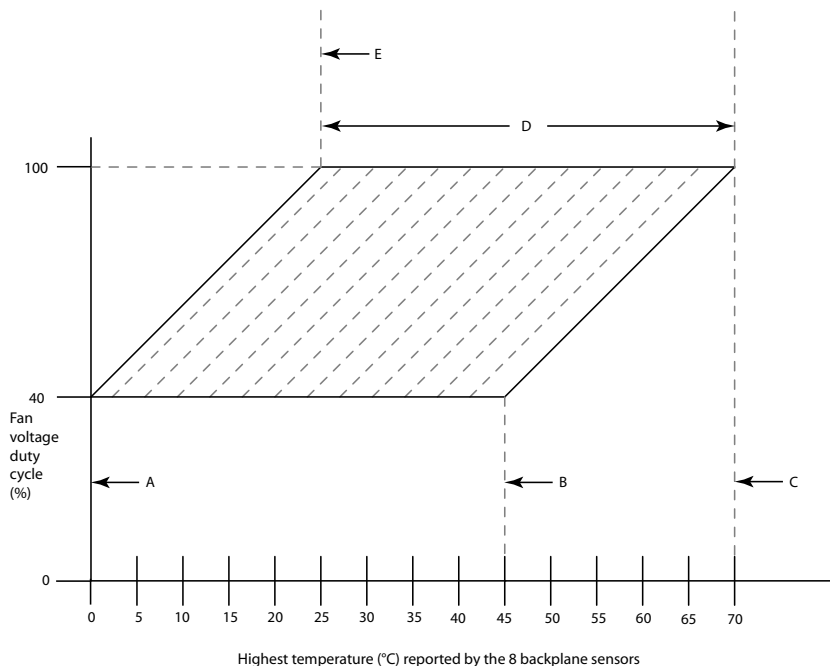
### 3.1.3 Smart Fan

When the Fan switch on the rear panel is set to AUTO, fans run at different speeds based on the measured temperature.

Target Temp indicates the temperature at which fans run at 100%. Using the default 50°C as an example, fans run at 40% when all temperature readings are less than 25°C, and begin ramp-up when any reading exceeds 25°C.

Fans run at 100% speed if any temperature reading exceeds 50°C (e.g., Target Temperature).

Setting parameters are as shown.



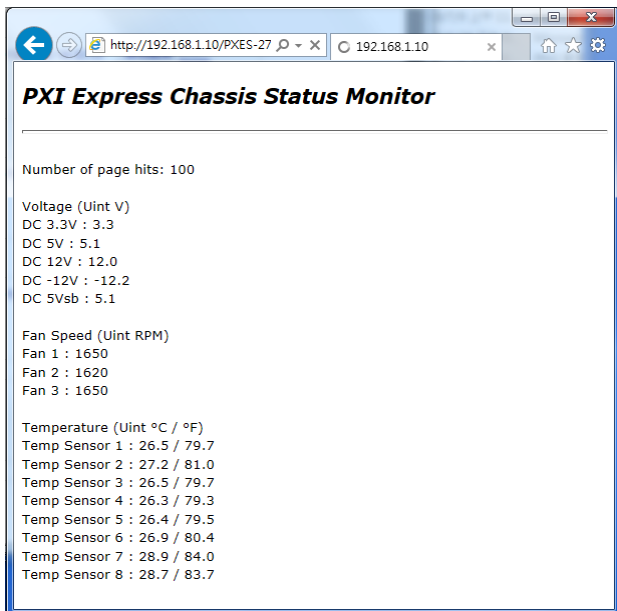
**Figure 3-3: Target Temperature Parameters**

Mark	Temperature	Event
A	0°C / 32°F	Lowest chassis temperature at which fan speeds commence ramping up for final 25° temperature mark (see E)
B	45°C / 113°F	Highest chassis temperature at which fan speeds commence ramping up for final 70° temperature mark (see C)
C	70°C / 158°F	Highest chassis temperature at which fan speeds reach maximum speed
D	25°C to 70°C / 77°F to 158°F (temperature range)	Range over which maximum chassis temperature (at which fan speeds reach maximum speed) can be set
E	25°C / 77°F	Lowest chassis temperature at which fan speeds reach maximum speed

**Table 3-1: Target Temperature Parameters Legend**

## 3.2 Remote Management LAN Port

The remote management LAN port located at the rear of the chassis is connected to an embedded web service that is built into the PXIe-62785. The webpage shows real-time chassis status.



**Figure 3-4: Chassis Status Monitor**

LAN Port Default IP Settings	
Static IP Address	192.168.1.10
Netmask	255.255.255.0

**Table 3-2: LAN Port Default IP Settings**

The IP settings (DHCP/Static IP, IP address, netmask, gateway, and MAC address) can be set in ChassisWatch. For more information about ChassisWatch IP settings, please refer to the JYTEK PXI Platform Services user manual.

## Appendix A - Maintenance

This Appendix describes basic maintenance and troubleshooting instructions for the PXIe-62785 chassis.

### A.1 Installation Problems

Inability to start the system frequently results from incorrect installation of the system controller, peripheral modules, and other components. Before starting the system, please ensure that:

- ▶ The system controller is properly installed and secured.
- ▶ All peripheral modules are properly seated on the slots.
- ▶ All cables are properly connected to the system controller and peripheral modules.
- ▶ All installed peripheral modules are compatible for use in the chassis.
- ▶ The power cord is securely plugged into the chassis power connector and power outlet/wall socket/power strip.

If the system fails to start when all installation conditions are met, remove all installed peripheral modules and try again. If the system starts normally, install one peripheral module at a time followed by powering up. You may also try installing the modules into different slots until the desired result is obtained.

## A.2 Basic Troubleshooting

Problem	Ensure that:
System fails to power up	<ul style="list-style-type: none"> <li>▶ The power cord is securely plugged into the chassis power connector and wall socket/power strip</li> <li>▶ The wall socket/power strip is live</li> <li>▶ The power button on the chassis front panel is activated</li> </ul>
No video output in the external display	<ul style="list-style-type: none"> <li>▶ The external display is functioning properly</li> <li>▶ Display settings support external video.</li> </ul>
Power LED (blue) is blinking	<ul style="list-style-type: none"> <li>▶ There is no short circuit by removing all PXI modules (PXI controller and peripheral modules)</li> </ul> <p>If the signal persists, contact your dealer for further assistance</p>
Fan LED (green) is blinking	<ul style="list-style-type: none"> <li>▶ The fan is unobstructed</li> </ul> <p>If the signal persists, contact your dealer for further assistance.</p>
Temperature LED (amber) is blinking	<ul style="list-style-type: none"> <li>▶ Airflow from the outlet apertures is unobstructed and steady; if not, ensure that adequate clearance for the intake apertures is provided</li> </ul> <p>If the temperature of exhausted air is normal (70°C in Default setting) but the temperature LED is still blinking, contact your dealer for further assistance.</p>



## **A.3 Maintenance**

### **A.3.1 Handling the Chassis**

The PXIe-62785 is designed for both rack-mount and bench top use. When transporting or carrying the chassis, it is recommended that the handle be used, being designed to support the weight of the chassis for superior portability and balance.

The PXIe-62785 weights 11.9 kg (26.2 lb). Please be careful when moving the chassis to avoid any possible injury.

### **A.3.2 Cleaning the Exterior**

Make sure that the system is turned off before cleaning the chassis exterior. Wipe the exterior with a clean cloth starting from areas that easily accumulate dust or dirt such as the area in and around the chassis and power supply air intake apertures.

### **A.3.3 Power Requirements**

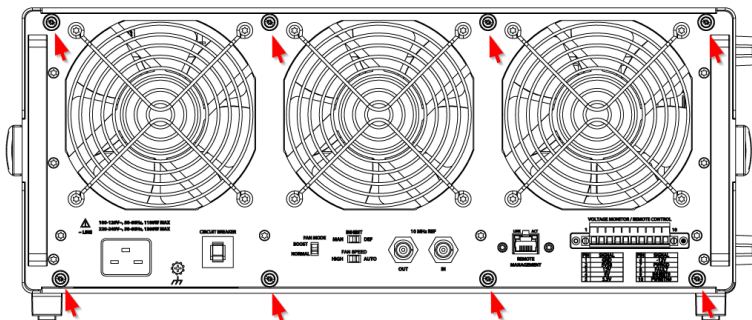
Make sure that the power cord is in good condition before plugging it into the system. It is important to check the reliability of the power source. The PXIe-62785 power supply is capable of handling 100 to 240 V AC within the 50 Hz to 60 Hz range. Do not connect the PXIe-62785 to an already overloaded circuit.

### A.3.4 Replacing the Power Supply Module

The power supply module of PXIe-62785 is with removable design that can be replaced in the field based on maintenance requirements. The power supply module can be ordered individually as backup part. Please contact your JYTEK sales window for more information.

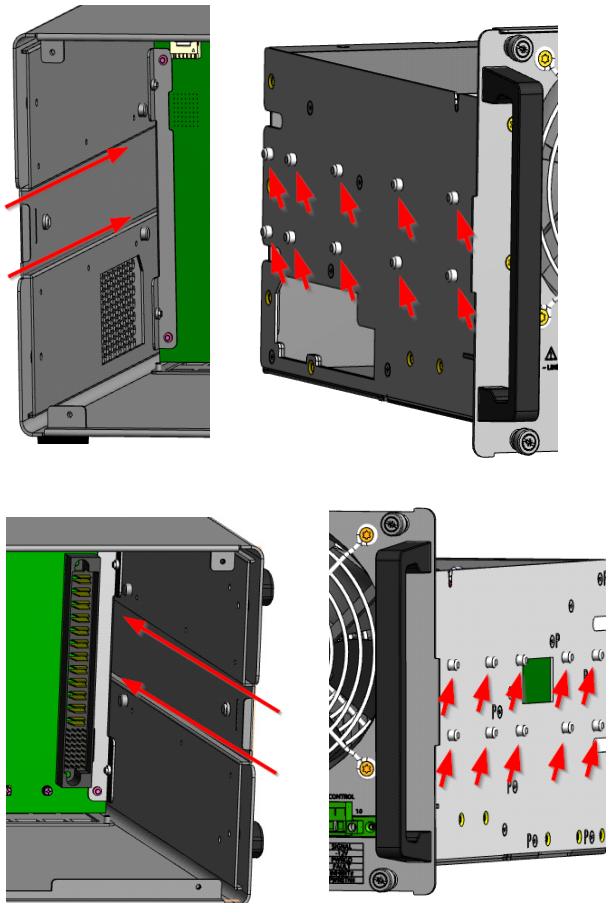
Steps for replacing the power supply module:

1. Shut down PXIe-62785.
2. Unplug AC power cord from the PXIe-62785.
3. Remove 8 back panel screws as shown.



4. With the chassis housing firmly secured, carefully pull the two power supply module handles (at rear of chassis) outward to remove the power supply module.

- When inserting a power supply module, align the power supply module guide pins with the guide rails inside the chassis, and carefully slide the power supply module into the chassis.



- Ensure the power module is fully inserted into the chassis.



## Important Safety Instructions

For user safety, please read and follow all instructions, Warnings, Cautions, and Notes marked in this manual and on the associated device before handling/operating the device, to avoid injury or damage.

*S'il vous plaît prêter attention stricte à tous les avertissements et mises en garde figurant sur l'appareil , pour éviter des blessures ou des dommages.*

- ▶ Read these safety instructions carefully.
- ▶ Keep the User's Manual for future reference.
- ▶ Read the Specifications section of this manual for detailed information on the recommended operating environment.
- ▶ The device can be operated at an ambient temperature of 55°C.
- ▶ When installing/mounting or uninstalling/removing device, or when removal of a chassis cover is required for user servicing (See "Getting Started" on page 29.):
  - ▷ Turn off power and unplug any power cords/cables.
  - ▷ Reinstall all chassis covers before restoring power.
- ▶ To avoid electrical shock and/or damage to device:
  - ▷ Keep device away from water or liquid sources.
  - ▷ Keep device away from high heat or humidity.
  - ▷ Keep device properly ventilated (do not block or cover ventilation openings).
  - ▷ Always use recommended voltage and power source settings.
  - ▷ Always install and operate device near an easily accessible electrical outlet.
  - ▷ Secure the power cord (do not place any object on/over the power cord).
  - ▷ Only install/attach and operate device on stable surfaces and/or recommended mountings.
- ▶ If the device will not be used for long periods of time, turn off and unplug it from its power source
- ▶ Never attempt to repair the device, which should only be serviced by qualified technical personnel using suitable tools

- ▶ A Lithium-type battery may be provided for uninterrupted backup or emergency power.



Risk of explosion if battery is replaced with one of an incorrect type; please dispose of used batteries appropriately.

*Risque d'explosion si la pile est remplacée par une autre de type incorrect. Veuillez jeter les piles usagées de façon appropriée.*

- ▶ The device must be serviced by authorized technicians when:
  - ▷ The power cord or plug is damaged.
  - ▷ Liquid has entered the device interior.
  - ▷ The device has been exposed to high humidity and/or moisture.
  - ▷ The device is not functioning or does not function according to the User's Manual.
  - ▷ The device has been dropped and/or damaged and/or shows obvious signs of breakage.
- ▶ Disconnect the power supply cord before loosening the thumbscrews and always fasten the thumbscrews with a screwdriver before starting the system up.
- ▶ It is recommended that the device be installed only in a server room or computer room where access is:
  - ▷ Restricted to qualified service personnel or users familiar with restrictions applied to the location, reasons therefor, and any precautions required.
  - ▷ Only afforded by the use of a tool or lock and key, or other means of security, and controlled by the authority responsible for the location.

	<p><b>BURN HAZARD</b></p> <p>Touching this surface could result in bodily injury. To reduce risk, allow the surface to cool before touching.</p> <p><b>RISQUE DE BRÛLURES</b></p> <p><i>Ne touchez pas cette surface, cela pourrait entraîner des blessures.</i></p> <p><i>Pour éviter tout danger, laissez la surface refroidir avant de la toucher.</i></p>
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## Getting Service

Customer satisfaction is our top priority. Contact us should you require any service or assistance.

### SHANGHAI JYTEK CO.,LTD.

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